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September 3, 2010

Assistant Commissioner
of Patents
Washington, D.C. 20231

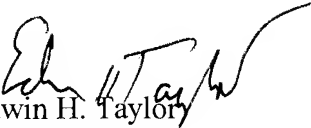
Re: **CERTIFICATE OF CORRECTION**
U. S. Letters Patent No. 7,484,044
Issued: January 27, 2009
For: Method and Apparatus for Joint Cache Coherency
States in Multi-Interface Caches
Inventor: Gilbert et al.
Our File No. 42390.P17020

Dear Sir:

Enclosed is a Certificate of Correction for the above-referenced patent.
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C.
Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP


Edwin H. Taylor
Reg. No. 25,129

EHT/rav
enclosures

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,484,044

DATED : January 27, 2009

INVENTOR(S) : Gilbert et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 9, at line 26 delete , -- stare — and insert “state”.

MAILING ADDRESS OF SENDER
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PATENT NO. 7,484,044

Certificate of Correction (PTO Form 1050)-Amended